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FIG. 1A
Prior Art

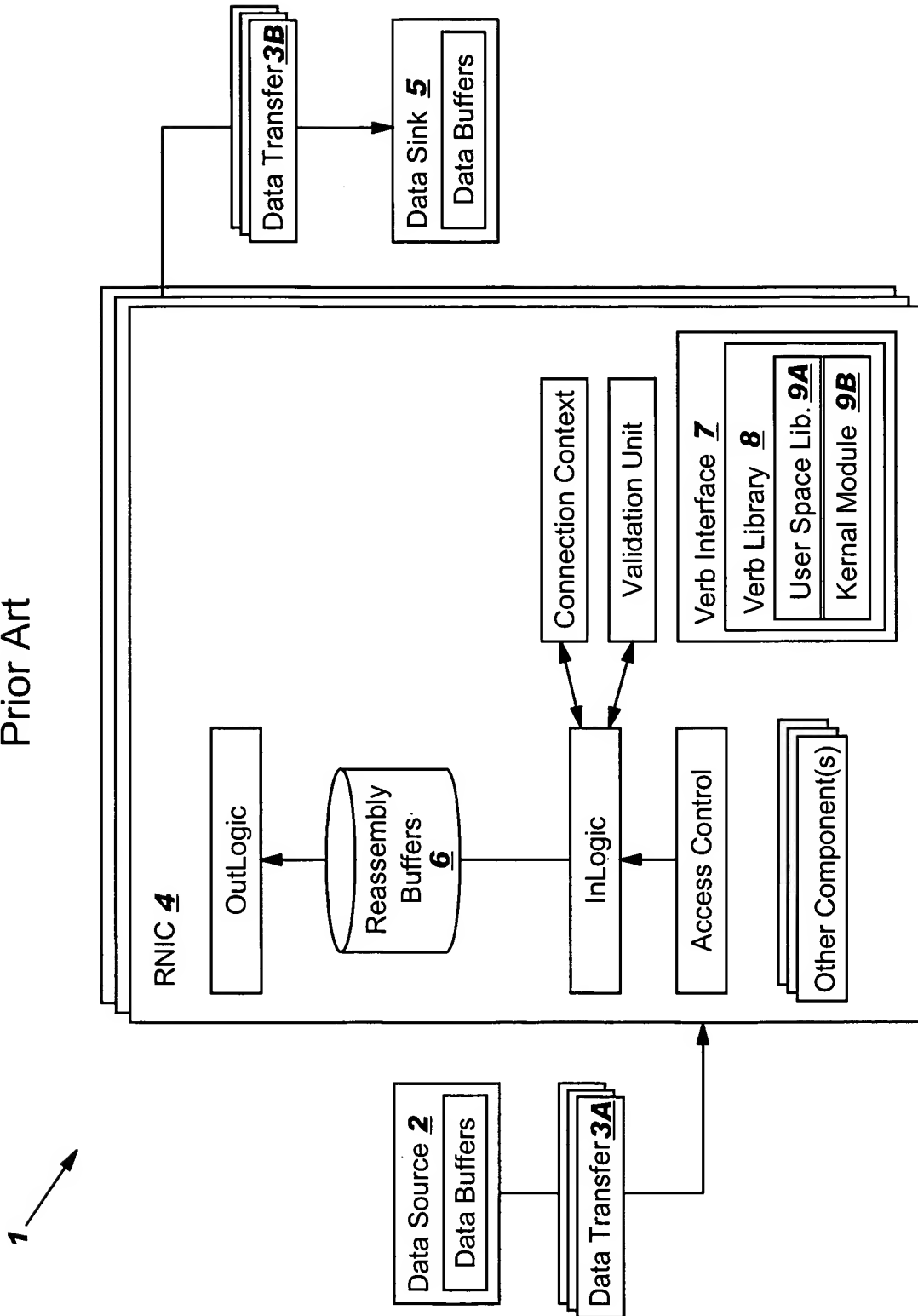
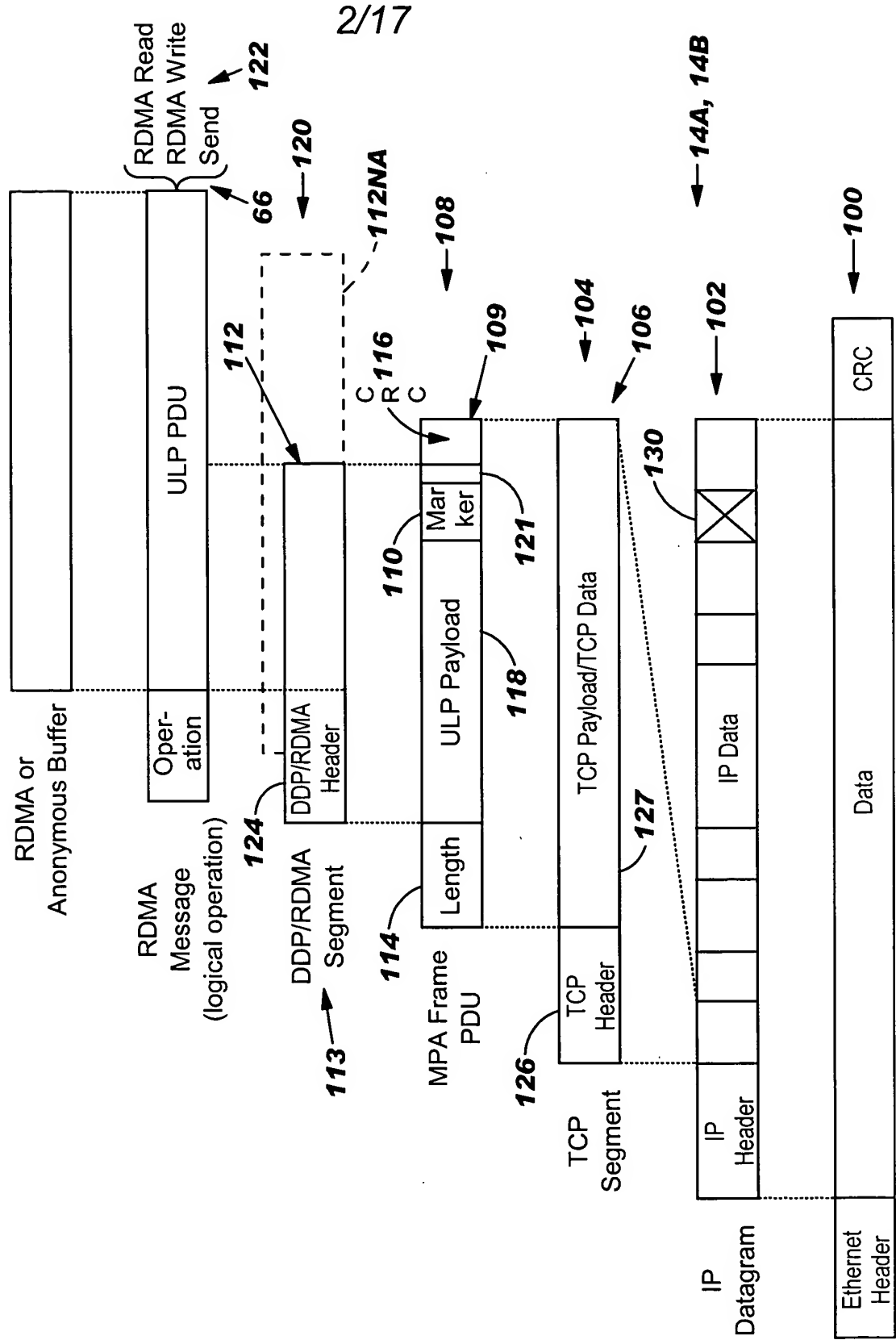
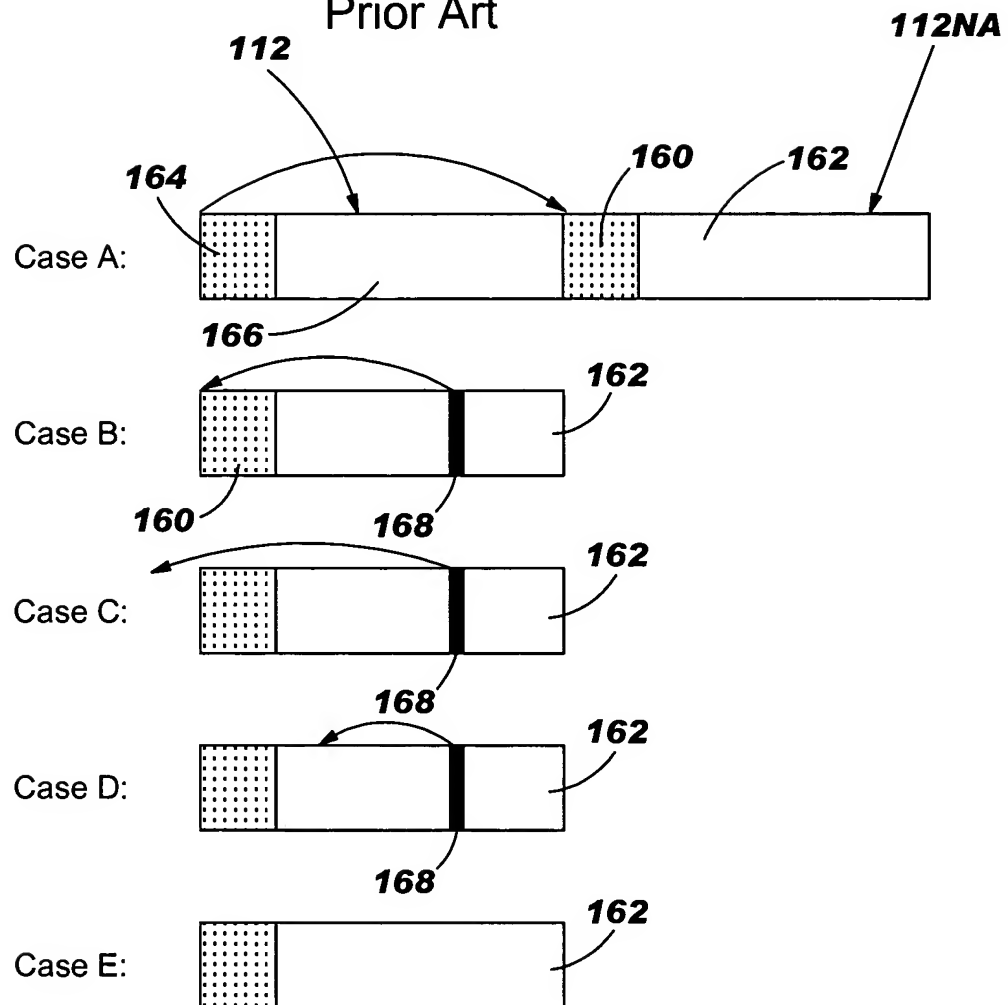


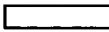


FIG. 1B
Prior Art



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FIG. 1C
Prior Art



Legend:

-  DDP payload
-  DDP header
-  Marker

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FIG. 1D
Prior Art

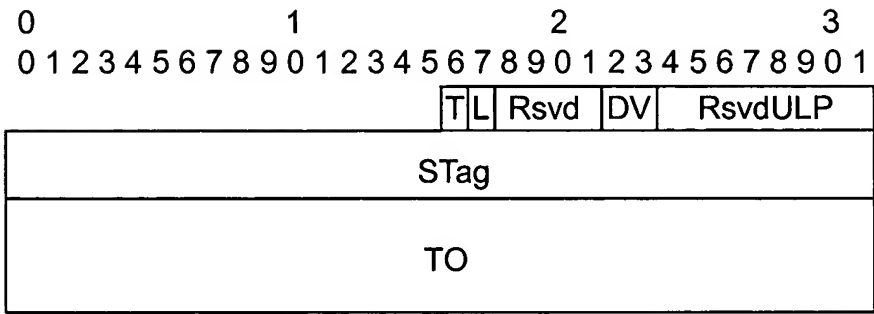
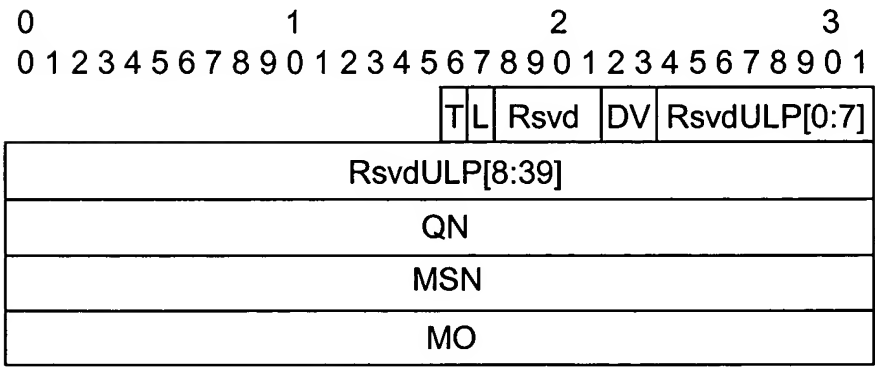
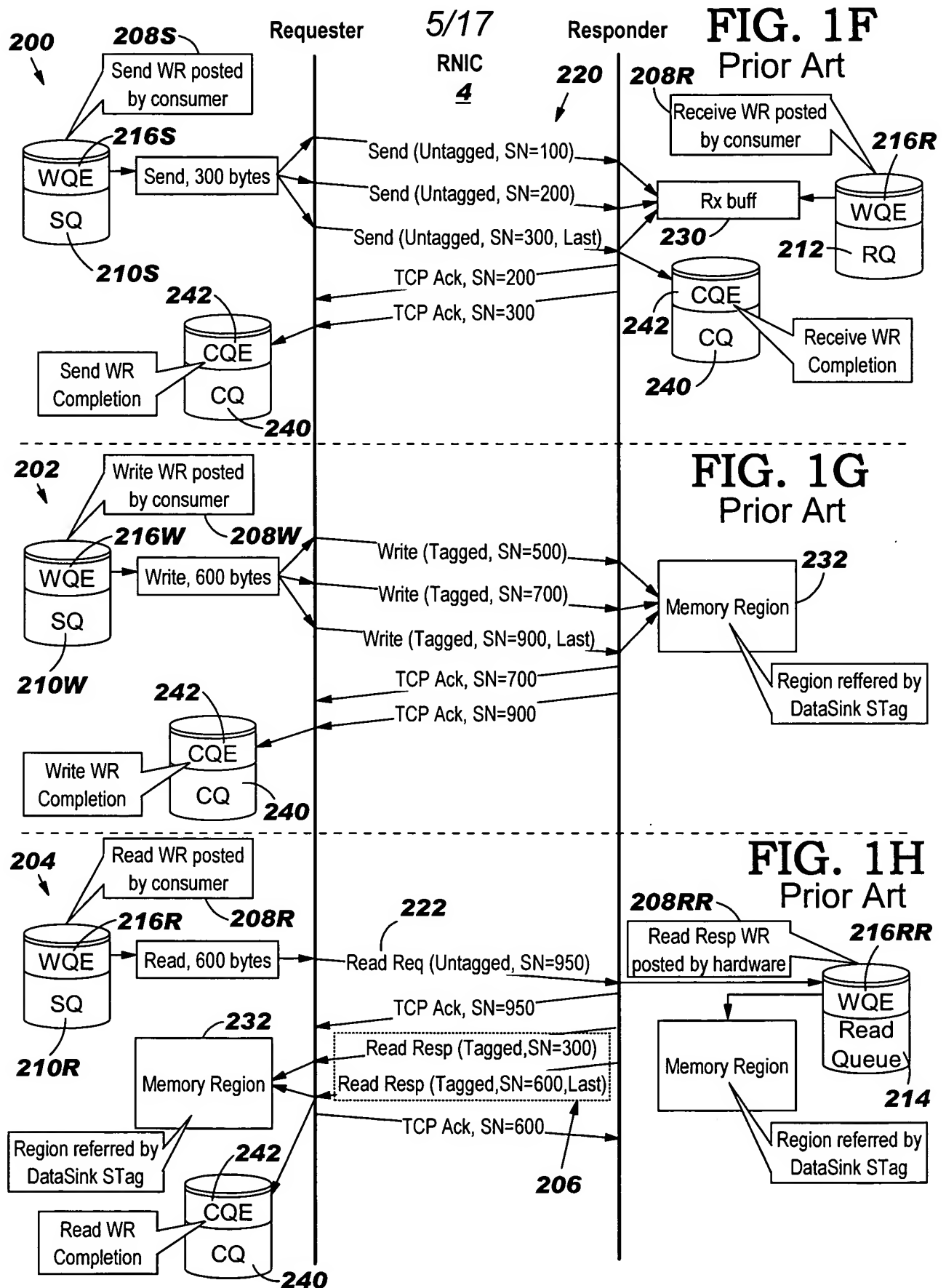


FIG. 1E
Prior Art





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FIG. 2A

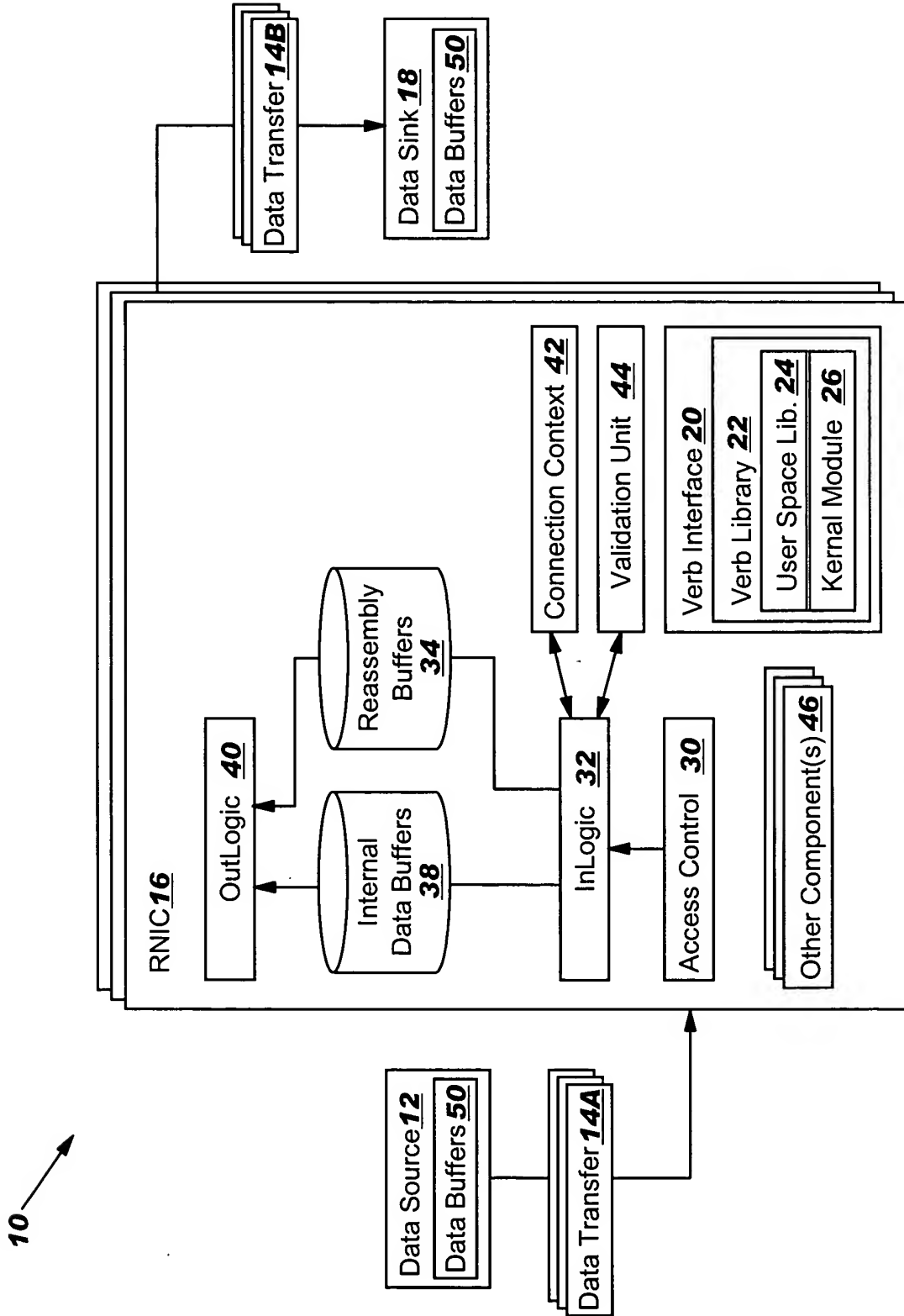


FIG. 2B

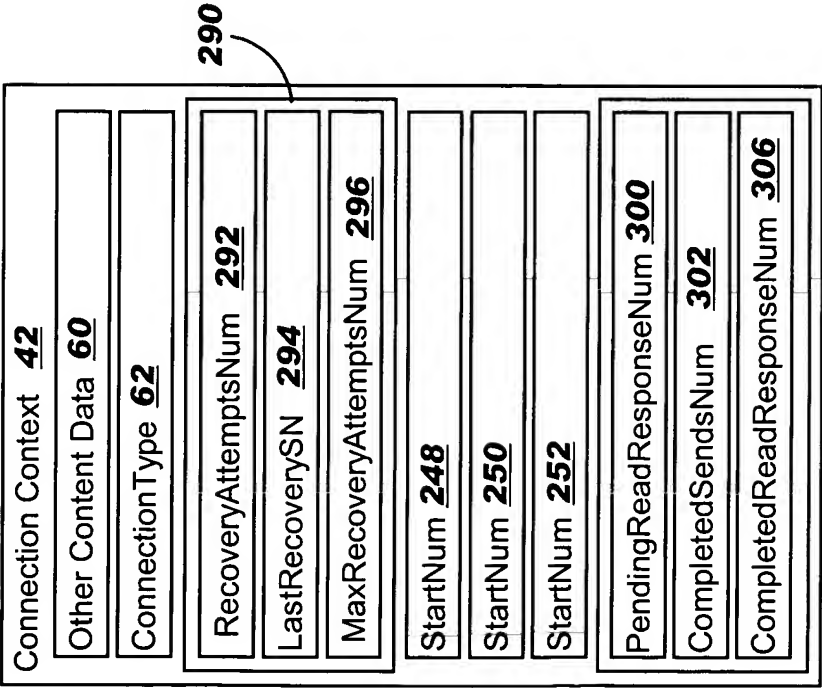
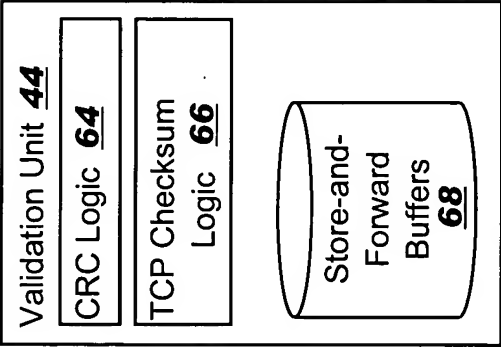
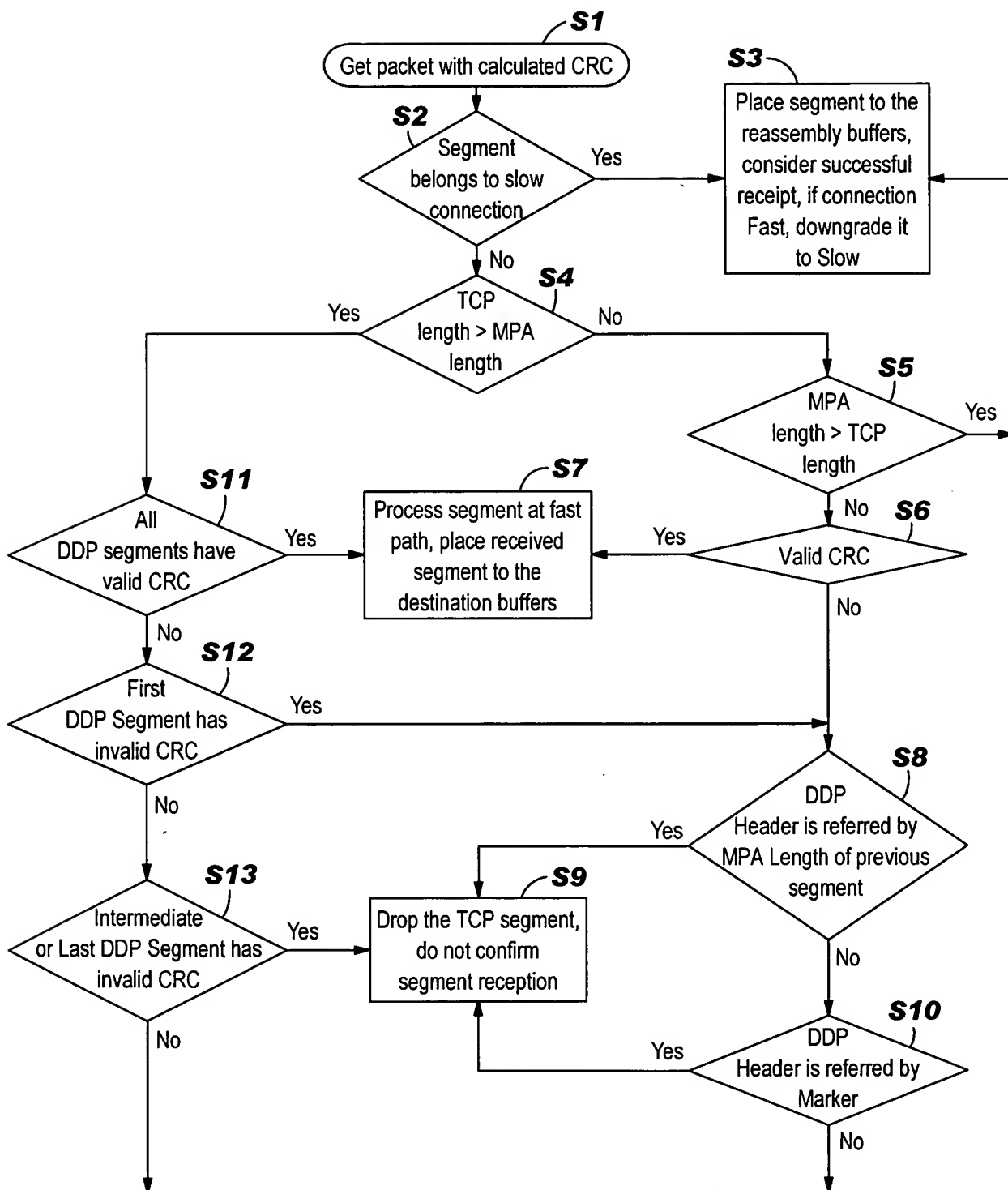


FIG. 2C

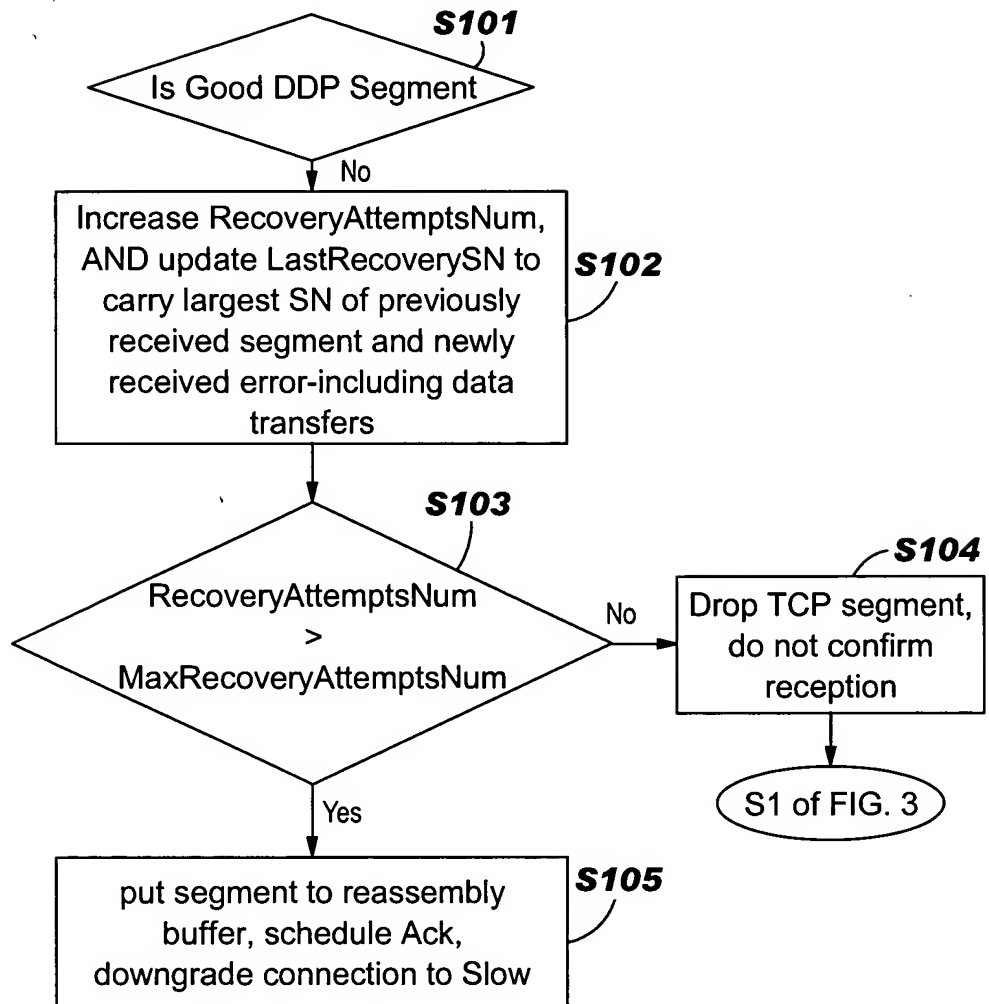
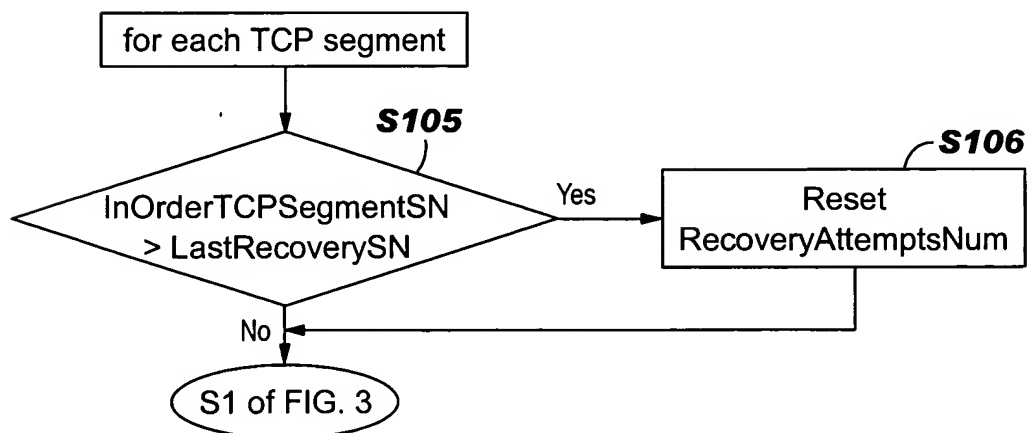


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FIG. 3

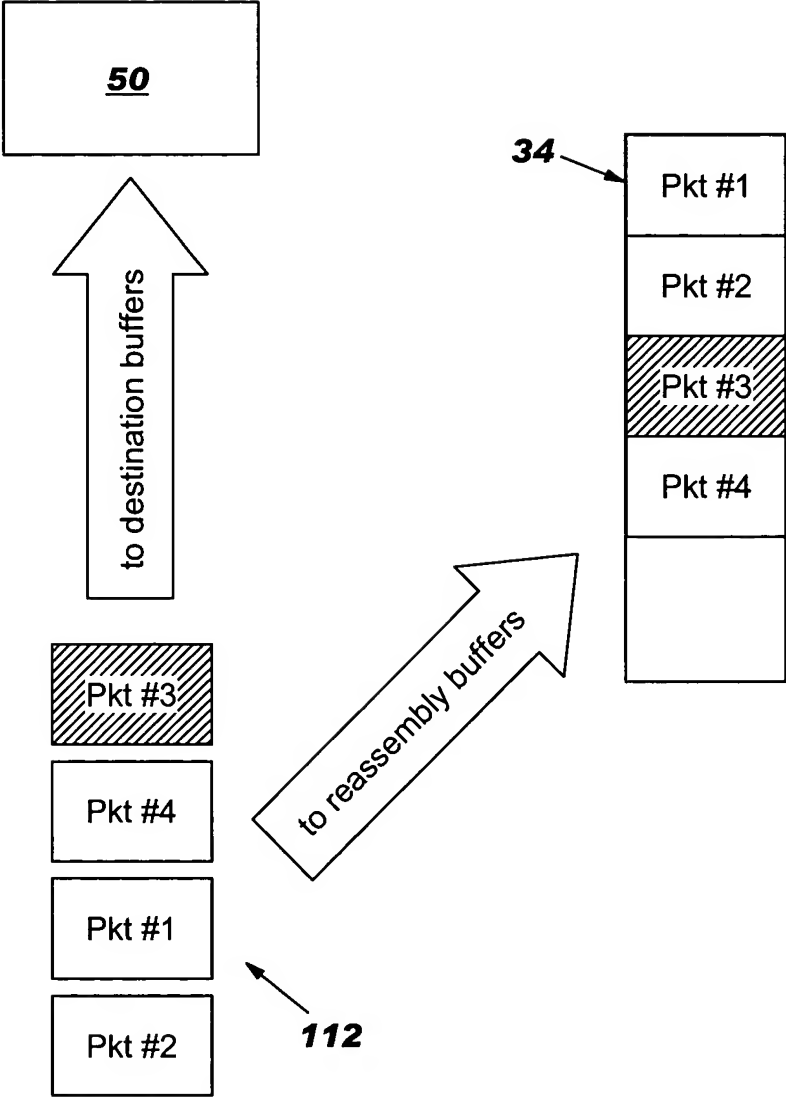


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FIG. 4A**FIG. 4B**

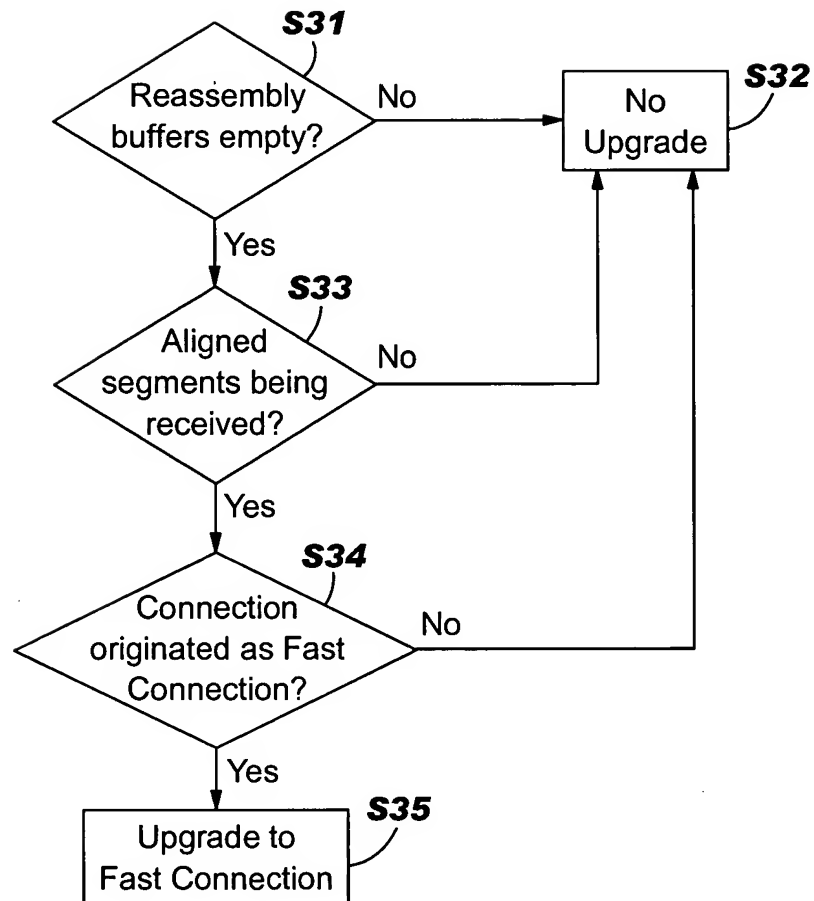
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FIG. 5



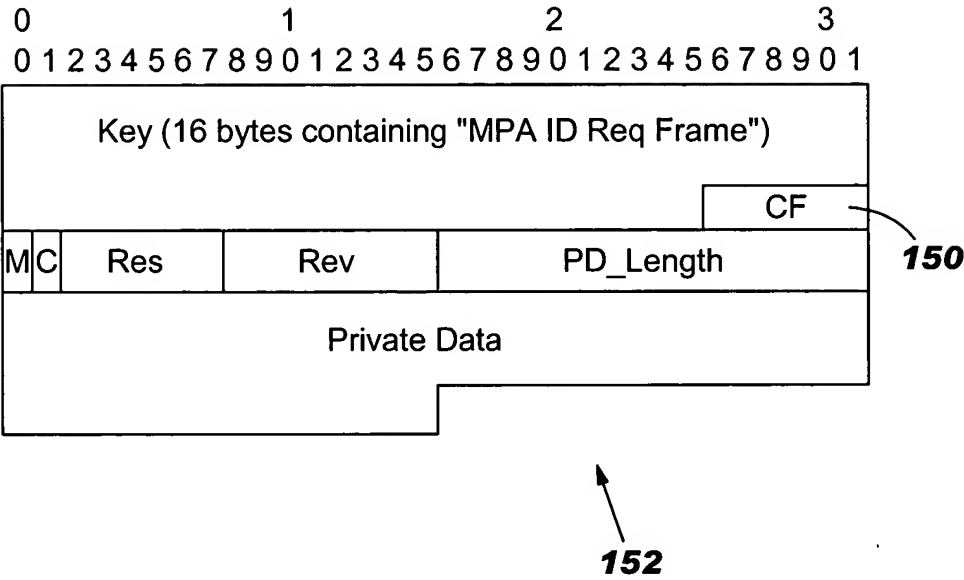
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FIG. 6



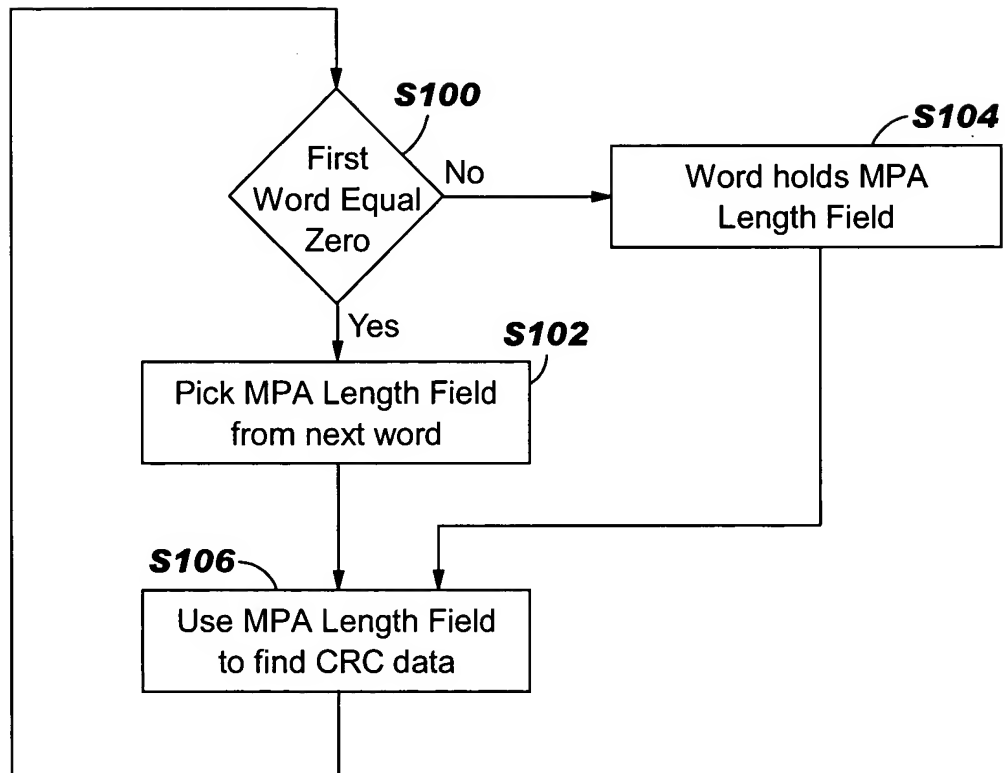
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FIG. 7



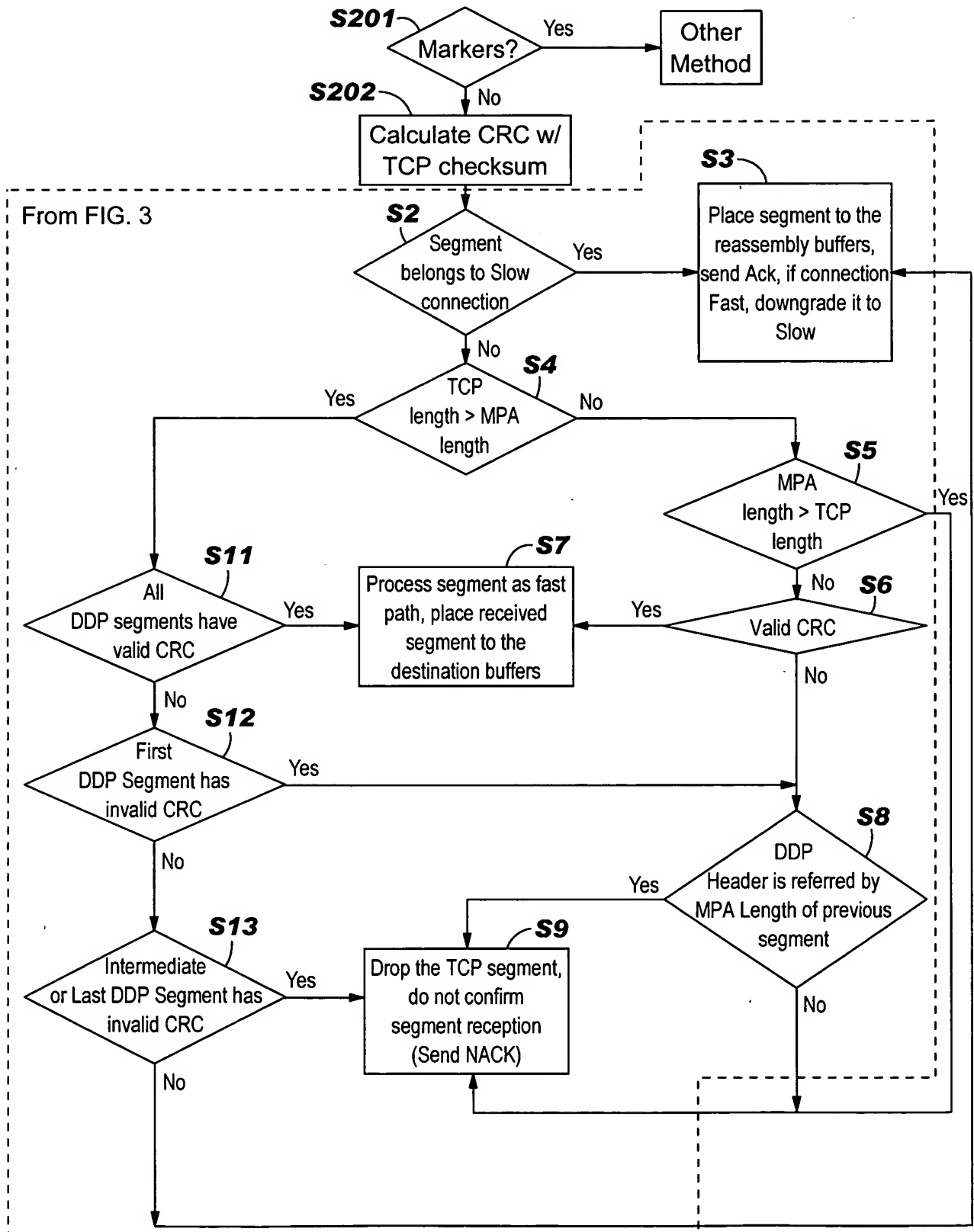
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FIG. 8



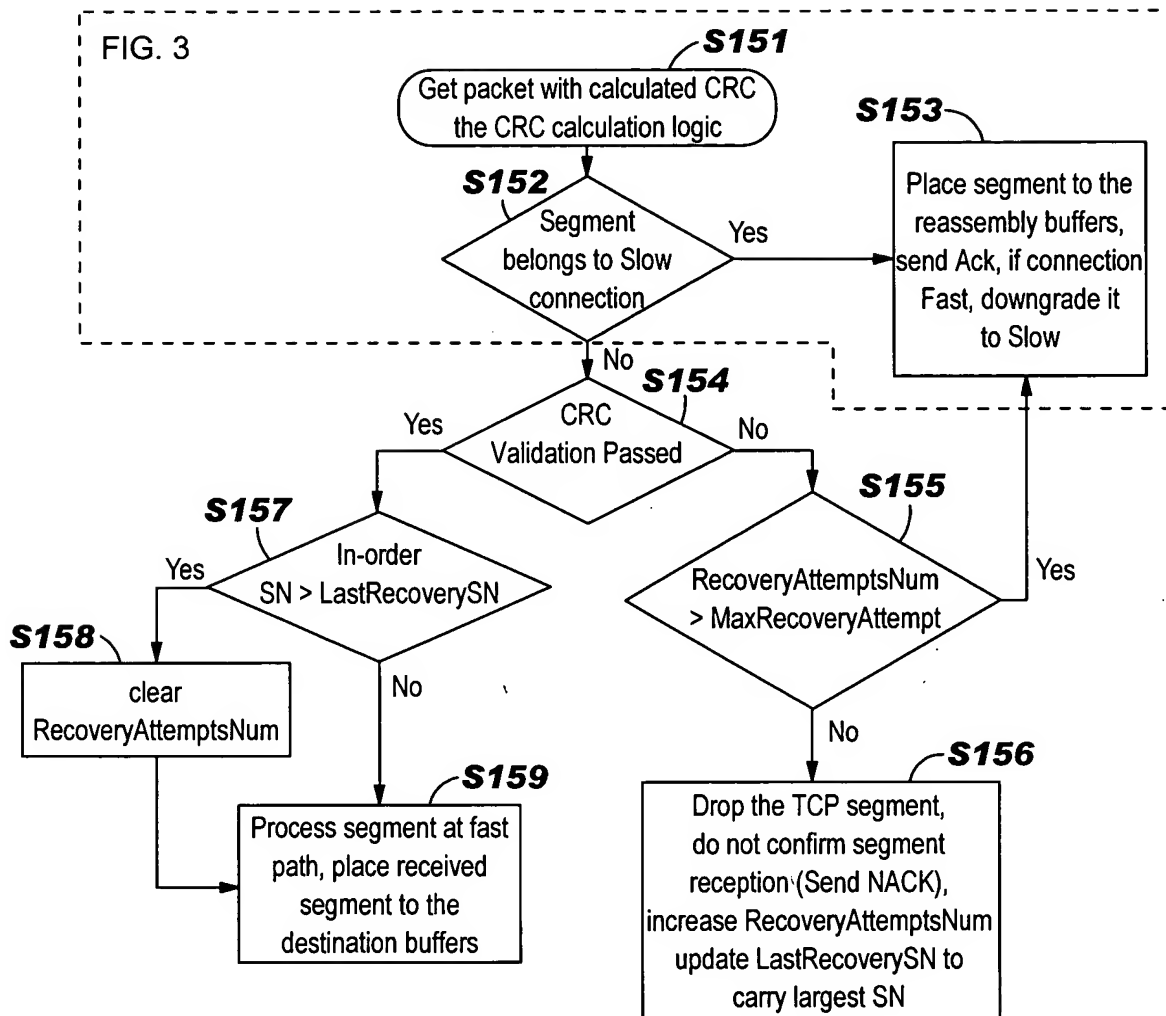
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FIG. 9



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FIG. 10



The diagram illustrates the flow of data and control messages between a consumer and hardware. It is divided into two main sections by a vertical line, representing the consumer side (left) and the hardware side (right).

Consumer Side (Left):

- 204:** Read WR posted by consumer.
- 210R:** A cylinder representing a queue with two sections: WQE (Write Queue Element) and SQ (Send Queue).
- 216R:** An arrow labeled "Read, 600 bytes" points from the WQE section of 210R to the hardware side.
- 208R:** An arrow labeled "Read Req (Untagged, SN=950)" points from the hardware side to the consumer side.
- 222:** An arrow labeled "TCP Ack, SN=950" points from the hardware side to the consumer side.
- 232:** A box labeled "Memory Region" with an arrow pointing to it from the hardware side.
- 242:** A cylinder representing a queue with two sections: CQE (Completion Queue Element) and CQ (Completion Queue).
- 240:** An arrow labeled "Read WR Completion" points from the CQE section of 242 to the consumer side.
- 206:** A dashed box labeled "Read Resp (Tagged, SN=300)" and "Read Resp (Tagged, SN=600, Last)" with an arrow pointing to it from the hardware side.
- 230:** An arrow labeled "TCP Ack, SN=600" points from the hardware side to the consumer side.
- 234:** A box labeled "Region referred by DataSink STag" with an arrow pointing to it from the hardware side.

Hardware Side (Right):

- 208RR:** Read Resp WR posted by hardware.
- 216RR:** A cylinder representing a queue with two sections: WQE (Write Queue Element) and Read Queue.
- 414:** A box labeled "Memory Region" with an arrow pointing to it from the hardware side.
- 416:** A box labeled "Region referred by DataSink STag" with an arrow pointing to it from the hardware side.

FIG. 12

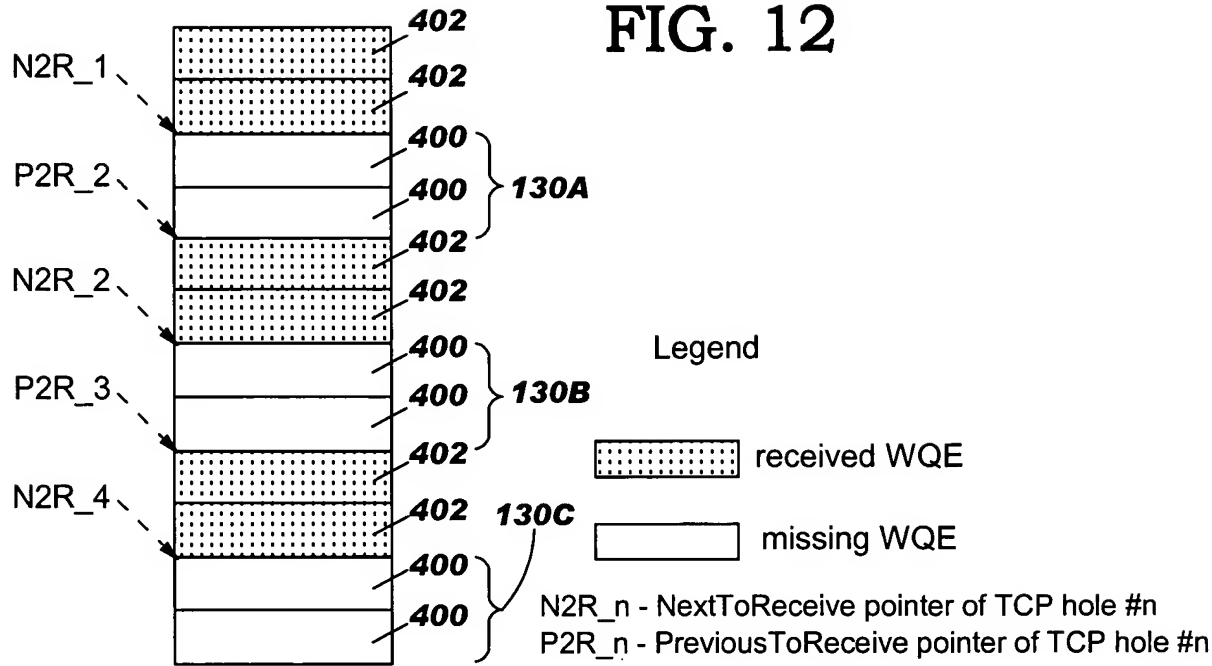


FIG. 13

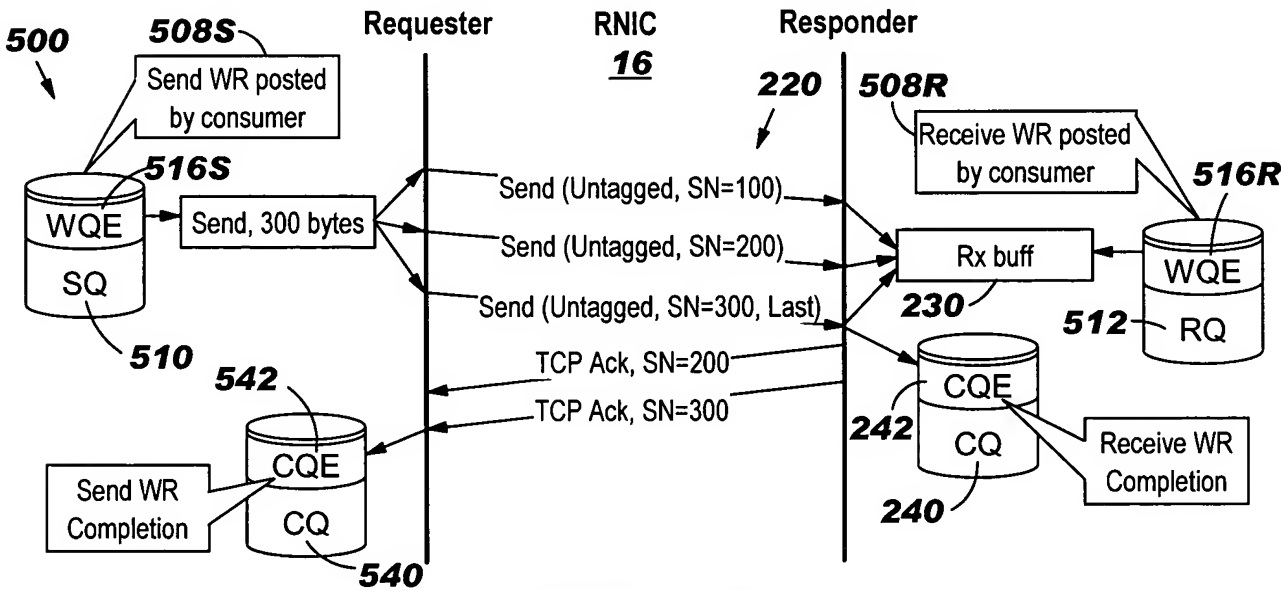


FIG. 14

